REMARKS

Claims 1-23 are pending in the present case. Claims 9, 15 and 16 are amended herein. Claims 1-8 and 23 are allowed. Applicants respectfully request reconsideration in view of the above amendments to the present application, and the arguments set forth below. No new matter is added herein.

ALLOWABLE SUBJECT MATTER

Applicant respectfully thanks the Examiner for allowing Claims 1-8 and 23, and for pointing out the Allowable Subject Matter of Claims 9-22. Claims 9 and 16 are amended herein to comply with 35 USC 112.

OBJECTIONS TO THE DRAWINGS

The drawings are objected to under 37 CFR § 1.83(a). Figure 4 has been corrected to include the item '225' as described in the specification. A Replacement Sheet is attached hereto for Figure 4 wherein the reference signs --225-- is added. Applicants respectfully assert that the replacement sheet complies with 37 CFR §§ 1.84(c) and 1.121(d) and that, as corrected therein, the drawings comply with 37 CFR § 1.83(a) and MPEP § 608.02(d). Applicants respectfully request the Examiner's review and approval.

OBJECTIONS TO CLAIMS

Claims 9 is objected to for an informality. Claim 9 is amended herein as discussed below. As amended herein, Claim 9 is free of the objected to informality. Applicants respectfully request the Examiner's review and approval.

CLAIM REJECTIONS

Claims 9 and 16 are rejected under 35 USC § 112 (¶ 2). Claims 10-15 and 17-22 are rejected also under this statute, for their dependence on Claims 9 and 16, respectively. Claims 9 and 16 are amended herein to read as shown below, with underlining added for emphasis.

9. A computer system comprising:
a bus;
a processor coupled to said bus; and
a computer-readable memory unit coupled to said bus, wherein said processor performs a method for decoding a sequence of binary digits <u>of a received signal</u>, said method comprising:
accessing said received signal comprising data and a sample;
extracting a data bit from said received signal;
accessing a received table;
indexing said received table to map said data bit to a corresponding known value;
calculating a sum comprising a squared difference between said sample and said known value;
storing said sum into <u>said computer-readable</u> memory unit; and
selecting a codeword from a memory resident table corresponding to a minimum value of said sum.

16. A computer usable medium having a computer readable program code embodied therein for causing a computer system to perform a method <u>for decoding a sequence of binary digits of a received signal, said method</u> comprising:
accessing said received signal comprising data and a sample;
extracting a data bit from said received signal;
accessing a received table;
indexing said received table to map said data bit to a corresponding known value;
calculating a sum comprising a squared difference between said sample and said known value;
storing said sum into a memory unit; and

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selecting a codeword from a memory resident table
corresponding to a minimum value of said sum.
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Applicants respectfully assert that the phrase --of a received signal-- on line 6 of Claim 9 and --for decoding a sequence of binary digits of a received signal-- on line 3 of Claim 16 provide sufficient antecedent basis for the term "accessing said received signal" in both claims.

As amended herein, Applicants respectfully assert that Claims 9-22 are allowable under 35 USC § 112 (¶ 2).

CONCLUSION

By the rationale stated above, Applicants respectfully assert that Claims 9-22 are allowable under 35 USC § 112 and that Claim 9 is free of informalities. Accordingly, Applicants respectfully request that the rejection of Claims 9-22 under 35 USC 112 (¶ 2) and the objection to Claim 9 be withdrawn and that Claims 9-22 be allowed. Claims 1-8 and 23 are Allowed.

Please charge our deposit account No. 23-0085, for any unpaid fees.

Respectfully submitted,

WAGNER, MURABITO & HAO, LLP

Dated: Feb. 14, 2005

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